

Patent

Attorney Docket No. MTI-31041-A

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Alexandria, VA 22313 INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO.	Serial No.
	MTI-31041-A	10/046,497
	Applicant	Confirmation No.
	Er-Xuan Ping	8624
	Filing Date	Group Art Unit
	October 26, 2001	2814

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub-Class
TZ	A1	6,594,293 B1	07-15-03	Bulsara	372	43
	A2	6,479,875 B1	11-12-02	Gonzalez	257	396
	A3	6,072,226	06-06-00	Thakur et al.	257	506
	A4	5,841,150	11-24-98	Gonzalez et al.	257	3
	A5	5,780,906	07-14-98	Wu et al.	257	368
	A6	5,360,760	11-01-94	Hayashi	117	93
TZ	A7	5,057,888	10-15-91	Fazan et al.	357	23.6

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initial		Non-Patent Document
TZ	B1	Van Zant, Peter, Microchip Fabrication, A Practical Guide to Semiconductor Processing, 4 th Ed., McGraw-Hill, New York, NY (2000), Chapter 12 at pages 380-388.
TL	B2	Wolf et al., Silicon Processing for the VLSI Era, Vol. 1: Process Technology, 2 nd Ed., Lattice Press, Sunset Beach, CA (2000), Chapter 7 (Silicon Epitaxial Growth and Silicon on Insulator) at pages 225-264.

Examiner Initials	Date Considered
<i>[Signature]</i>	03/10/05
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

MKE/982651.1

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12-27-2004